

Amendments to the Specification

On page 6, Line 23 of the Specification, please AMEND the number [[46]] to read as 48 in the instant application as follows:

-- The outputs from the filters 42 and 44 are also introduced to stages [[46]]48 designated as a "Symbol Recovery Loop" and to stages 50 designated as a "Carrier Recovery Loop." The output from the Symbol Recovery Loop 48 controls the operation of the voltage controlled oscillator 38 and the output from the Carrier Recovery Loop 50 controls the operation of the voltage controlled oscillator 28. --

On page 6, Line 31 of the Specification, please AMEND the number [[40]] to read as 480 in the instant application as follows:

-- The carrier signals modulated by the data signals are received on the line 12. The modulated carrier signals are converted to an intermediate frequency (IF) of approximately [[40]]480 MHz by the tuner 14 and the IF signals are provided with a gain control as at 20. Inphase and quadrature components of these IF signals are then respectively produced in the multipliers 22 and 24. The carrier signals at the IF frequency are then removed from these signals at 30 and 32 so that only the information signals with the inphase and quadrature components remain.